

REMARKS

Reconsideration of the present application is respectfully requested.

The present application has been review in light of the Office Action dated September 15, 2004. Claims 1-5 are currently pending in the present application.

In the Office Action, Claims 1-5 were rejected under 35 U.S.C. §103(a) as being unpatentable over U.S. Patent No. 6,268,847 (*Glen*) in view of U.S. Patent No. 5,844,623 (*Iwamura*) and U.S. Patent No. 6,339,422 B1 (*Kuwajima*).

More specifically, the Examiner has rejected independent Claims 1 and 4 as being unpatentable over *Glen* in view of *Iwamura* and *Kuwajima*. In the rejection, the Examiner asserts that *Glen* teaches all the recitations of Claims 1 and 4, except for an OSD controller, which is allegedly disclosed in *Iwamura*, and a timing signal generator, which is allegedly disclosed *Kuwajima*. However, it is respectfully submitted that the Examiner is incorrect.

Claims 1 and 4 recite a first memory for storing YUV data and a second memory for storing RGB data. The Examiner asserts that the RGB conversion module 12 and the first color base conversion module 14 of *Glen* each inherently comprise “some sort of first and second memories for storing the RGB and YUV data, respectively.” However, it is respectfully submitted that the Examiner’s assertion is incorrect. After thoroughly reviewing *Glen*, it is respectfully submitted that there is no reference to the RGB or YUV data being stored in the conversion units.

Further, even assuming that the Examiner’s assertion of the inherency of the conversion units having a memory device is correct, based on the Examiner’s assertion that the converted data is stored for later mixing, it is respectfully submitted that even in this scenario the YUV data is not stored. That is, the YUV data is converted to RGB data in the first color base conversion module 14 of *Glen*. Therefore, if this data is then stored, it is RGB data being stored, not YUV data. Therefore, it is respectfully submitted that the Examiner is incorrect in asserting that *Glen* teaches a first

memory for storing YUV data and a second memory for storing RGB data, as recited in independent Claims 1 and 4 of the present application. Additionally, it is respectfully submitted that neither *Iwamura* nor *Kuwajima* cure this deficiency.

Accordingly, it is respectfully submitted that the Examiner is incorrect in rejecting Claims 1 and 4, and it is respectfully requested that the rejection of independent Claims 1 and 4.

Without conceding the patentability per se of dependent Claims 2, 3, and 5, it is submitted that they overcome the prior art by virtue of their dependencies on independent Claims 1 and 4. Accordingly, it is submitted that Claims 1-5 are patentable.

In view of the above remarks and amendments, reconsideration and allowance of Claims 1-5 is respectfully requested. Applicant submits that pending Claims 1-5 are believed to be in condition for allowance and allowance is respectfully requested. Should the Examiner believe that a telephone conference or personal interview would facilitate resolution of any remaining matters, the Examiner may contact Applicant's attorney at the number given below.

Respectfully submitted,



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